

SNAP Load Cell Modules

Features

- ▶ SNAP-AILC works with 2 mV/V or 3 mV/V load cells; SNAP-AILC-2 works with or 3 mV/V or 4 mV/V load cells
- ▶ Provides both direct ADC readings and filtered values
- ▶ Convenient pluggable wiring terminals
- ▶ Module converts power from an external 24 VDC power supply to isolated 10 VDC for bridge excitation for up to four 350 ohm load cells.
- ▶ Rugged packaging

Description

The SNAP-AILC and SNAP-AILC-2 modules each provide one channel of transformer and optically-isolated analog-to-digital conversion for load cell inputs.

These modules support 6- or 4-wire load cells (4-wire is at reduced performance). Both modules provide isolated 10 VDC bridge excitation for up to four parallel 415/350 ohm load cells as a load cell front end. For both modules, 24 VDC is required at the separate connector on top of the module.

The bridge and 10 VDC bridge power are isolated from the 24 VDC bridge source and the 5 VDC source used by the mounting rack. The 24 VDC input is reverse-polarity protected and may be used for multiple modules. The 10 VDC bridge source is current limited so that an external fault on the load cell is isolated and will not affect another load cell module.

There are two channels of data: channel 1 is unfiltered, and channel 2 is filtered by a 0 to -48dB programmable digital filter. The filtered data has a fast settling feature with a programmable fast settle level. This feature decreases settling time when there are large step changes in the load cell output and high filter weights. For more information, see [“Using the Filter Weight and Fast Settling Features” on page 3](#).

As part of the SNAP PAC System, these modules can be used with all SNAP PAC brains and rack-mounted controllers. The modules snap onto Opto 22 SNAP PAC mounting racks, and field connections are made by a pluggable terminal strip on top of the module.

The SNAP-AILC and SNAP-AILC-2 are configured using PAC Manager or PAC Control.



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Notes for legacy products: SNAP-AILC and SNAP-AILC-2 modules can also be used with legacy SNAP Ultimate, SNAP Ethernet, and SNAP Simple brains that have analog capability, and with SNAP M-series and B-series mounting racks. These modules cannot be used with the serial B3000 brain.

The SNAP-AILC is also supported by legacy software ioManager, and both modules are supported by ioControl. See the following table for minimum firmware and software versions required.

Firmware and Software Versions Required	
SNAP-AILC	SNAP-AILC-2
Processor firmware 6.1c or higher	Processor firmware 7.1d or higher
PAC Manager or ioManager 7.0a or higher	PAC Manager
PAC Control or ioControl 6.1c or higher	PAC Control or ioControl 7.1d or higher

For more information on legacy products, see Opto 22 form #1688, *SNAP PAC System Migration Technical Note*, and form #1693, *Legacy and Current Product Comparison and Compatibility Charts*.

Part Number

Part	Description
SNAP-AILC	Load cell input, 10 VDC excitation, 2 or 3 mV/V input sensitivity
SNAP-AILC-2	Load cell input, 10 VDC excitation, 3 or 4 mV/V input sensitivity

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Specifications

Input Range Sensitivity: SNAP-AILC SNAP-AILC-2	2 mV/V or 3 mV/V (Over range ± 2.2 mV or ± 3.3 mV) 3 mV/V or 4 mV/V (Over range ± 3.3 mV or ± 4.4 mV)
Input Resistance	100 Megohms differential
Resolution: Analog Scale Points	>22 bits + sign ($\pm 6,400,000$ counts = $\pm 100\%$ of scale)
Response Time, Data Freshness	See the following table
DC Reversal (Input)	0.015% (± 1000 counts of reading @ 6,400,000 F.S.)
DC Common Mode Rejection	>-120 dB
AC Common Mode Rejection	>-120 dB @ 60 Hz
Maximum Survivable Input	± 15 volts across module load cell connector
Maximum Operating Common Mode Voltage	250 V
Accuracy (% Full Scale): Offset Gain	$\pm 0.05\%$ (= 3,200 counts out of 6,400,000, out of box) $\pm 0.05\%$ (= 3,200 counts out of 6,400,000, out of box)
Usable Resolution at Default Configuration (Fast)	38 nV - sign and 19 bits ($\pm 524,288$ counts @ channel 2) At filter weight 64, settles to 99.9% of final reading in 3.9 s.
DRIFT: Gain Offset	40 PPM / °C 10 PPM / °C
Isolation: Optical	4000 V
Isolation: Transformer	1500 V
Rack Power Requirements	5.00 VDC to 5.20 VDC @ 120 mA
24V Bridge Supply: Input Voltage Input Current Output Fault Current	24 VDC nominal (22 V min. to 30 V max.) 40 mA for one load cell or 115 mA for four load cells 124 mA typical (field fault—shorted bridge)
Ambient Temperature: Operating Storage	0 to 70 °C -25 to 85 °C

SNAP-AILC and SNAP-AILC-2 Configuration Options

Both modules can be configured as "Fast" or "Slow," depending on the response you need. Configure modules in PAC Control or PAC Manager.

	Fast (Default)		Slow (Configuration option)	
ADC Notch Filter (Channel 1)	60 Hz (-3 dB @ 125 Hz)		20 Hz (-3 dB @ 42 Hz)	
Data Freshness (Channel 1)	9 ms		25.8 ms	
Noise Reduction (Channel 2)	At filter weight 64 (default): -3 dB T.C. = 567 ms	At filter weight 8: -18 dB T.C. = 72 ms	At filter weight 64: -3 dB T.C. = 1626 ms	At filter weight 8: -18 dB T.C. = 206 ms
Settle Time to % of Final Reading (Channel 2)	At filter weight 64 (default): 2.637 s to 99.0% 3.924 s to 99.9% 5.238 s to 99.99%	At filter weight 8: 0.31 s to 99.0% 0.47 s to 99.9% 0.62 s to 99.99%	At filter weight 64: 7.56 s to 99.0% 11.25 s to 99.9% 15.00 s to 99.99%	At filter weight 8: 0.9 s to 99.0% 1.3 s to 99.9% 1.8 s to 99.99%

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Using the Filter Weight and Fast Settling Features

There are two channels of data on the SNAP-AILC and SNAP-AILC-2 modules. The first channel provides raw Analog-to-Digital (ADC) data, while the second channel provides filtered data.

The filtered reading is calculated by the module from channel 1 data after every ADC conversion. Filtered output is calculated as follows:

$Y_{new} = (X - Y) / W + Y$	
Y _{new} =	new filtered reading
Y =	last filtered reading
X =	new ADC reading =
W =	filter weight (range: 0 to 255d)

You configure filtering in PAC Control software using these two commands: Set Analog Load Cell Filter Weight and Set Analog Load Cell Fast Settle Level.

Set Analog Load Cell Filter Weight

The Set Analog Load Cell Filter Weight command sets the filter weight on channel 2 of a load cell module to a range of 0 to 255. Noise is reduced by up to 255, at the expense of settling time.

For maximum resolution, set the filter weight to less than half the peak-to-peak system noise as seen while trending the data in PAC Display.

Set Analog Load Cell Fast Settle Level

The Set Analog Load Cell Fast Settle Level command sets the fast settling trigger level (FSTL). The filtered weight is reduced when the difference between the ADC data and the filtered data is greater than the FSTL. This feature is useful to decrease settling time when there are large step changes in the load cell output and a large filter weight.

For more information on the weight filtering and fast settling features, see the commands Set Analog Load Cell Filter Level and Set Analog Load Cell Fast Settle Level in the *PAC Control Command Reference* (Opto 22 form #1701).

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Dimensions

SNAP-AILC and SNAP-AILC-2 Modules

